

IN THE SPECIFICATION

Please amend the specification as follows:

Replace the paragraph on page 10, between lines 3-13 of the specification with the following:

The charging transistor 36 is also turned on. This couples the gate and drain of the first drive transistor 22, which is thereby turned on, in a diode-connected configuration. During the programming phase, the cathode of the display element 2 is at a raised potential, so that the display element 2 is reverse biased. Thus, the current driven by the second drive transistor 30 is driven through the first drive transistor 22. The circuit stabilises when the gate-source voltage of the ~~second~~first drive transistor 22 corresponding to the current driven by the ~~first~~second drive transistor 30 is stored on the capacitor 24. The voltage on the source of the first drive transistor 22 is able to float to allow this equilibrium to be reached. Thus, the first drive transistor 22 is current-addressed and a voltage sampling operation is carried out.